

Notice of References Cited	Application/Control No. 10/734,226	Applicant(s)/Patent Under Reexamination LEE ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,759,263	07-2004	Ying et al.	438/48
	B	US-6,452,764	09-2002	Abraham et al.	360/324.2
	C	US-6,083,794	07-2000	Hook et al.	438/286
	D	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 8-9.
	V	Streetman, Solid State Electronic Devices, 1990, Prentice Hall, 3rd ed., p. 323.
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.